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Sushu
XA-9630

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Tsuyoshi FUJIWARA et al.

Appln. No.: 10/083,416

Group Art Unit: 2813

Filed: February 27, 2002

Examiner: Thang Nguyen

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
MANUFACTURING METHOD THEREOF

* * *

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In response to the Office Action mailed February 28, 2003, please amend the above-identified patent application as indicated below.

IN THE TITLE:

Please change the title to read:

METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE COMPRISING A MEMORY CELL AND A CAPACITOR

TECHNOLOGY CENTER 2800

MAY 28 2003

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